

1 Filed

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hiroyuki Uchida

Serial No.: 09/874,296

Group Art Unit: 2811

Filed: June 6, 2001

Examiner: Crane, Sara W.



For: SURGE PROTECTION CIRCUIT FOR SEMICONDUCTOR DEVICES

Honorable Commissioner of Patents

**SUPPLEMENTAL AMENDMENT**

Sir:

Supplementing the Amendment filed May 24, 2004, please amend the above-identified application as follows: